

ABSTRACT

A plasma display panel free from blisters and pinholes on dielectric layers and excellent in characteristic of breakdown voltage. This plasma display panel has multilayered first dielectric layer (7) covering a display electrode including a scanning electrode and a sustain electrode provided on front substrate (3), and a multilayered second dielectric layer covering a data electrode provided on a back substrate, wherein periphery (21) of upper dielectric layer (7b) of first dielectric layer(7) and/or the second dielectric layer is positioned identically or partially in size and shape to periphery (22) of lower dielectric layer (7a) to be formed.